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FIG.1  
PRIOR ART

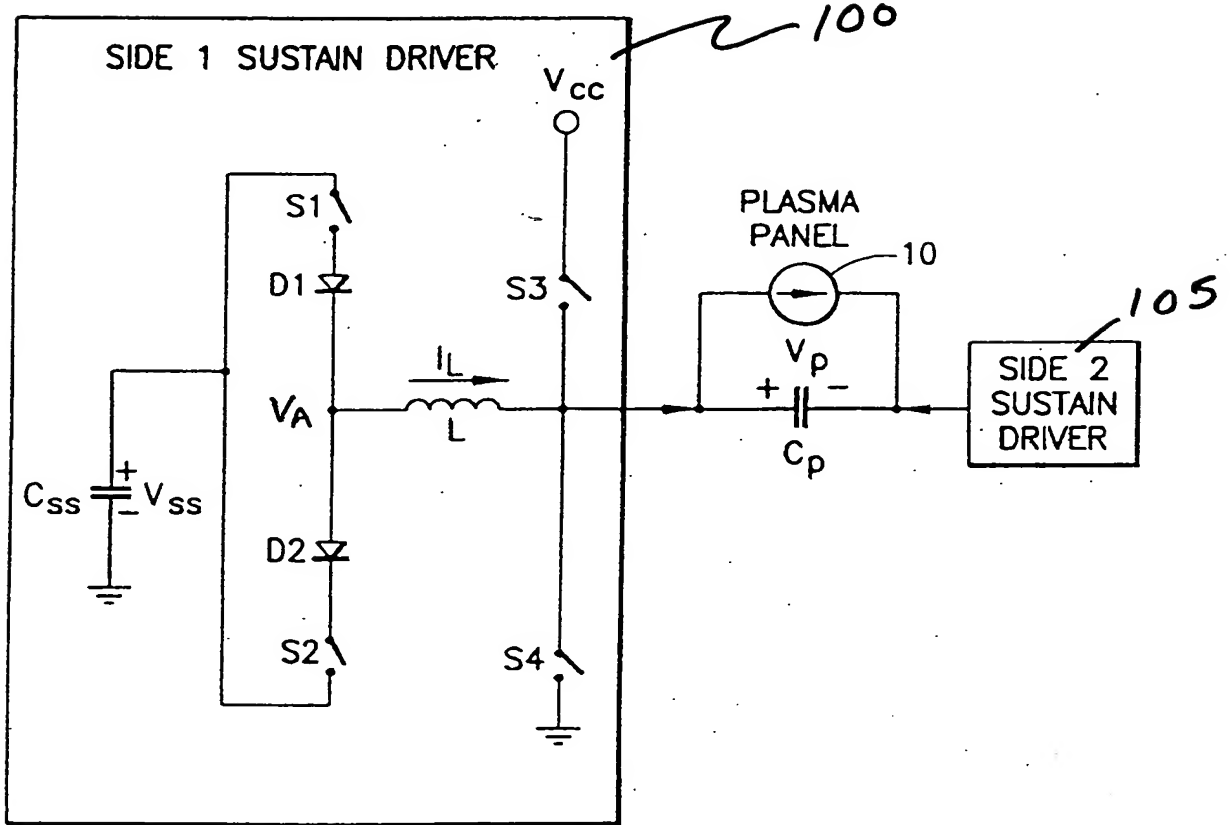
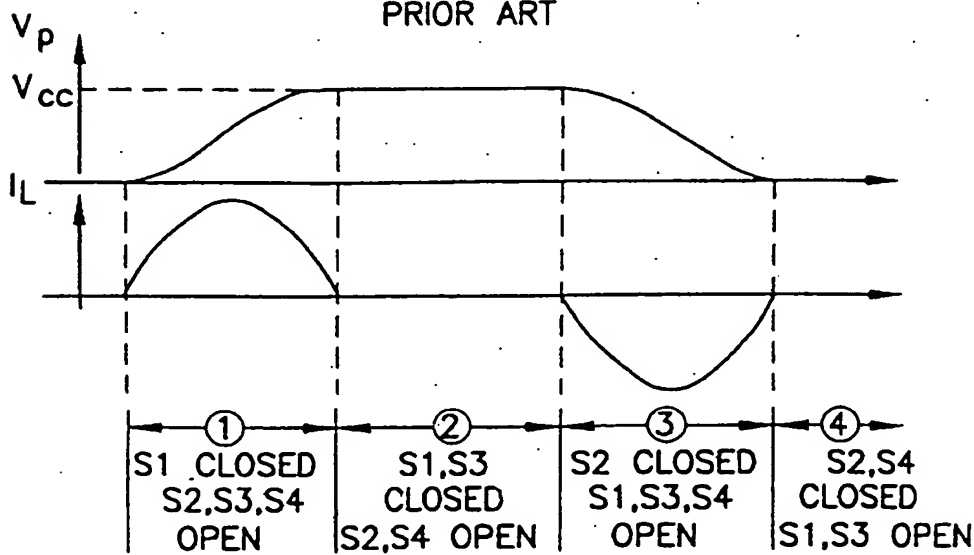


FIG.2  
PRIOR ART



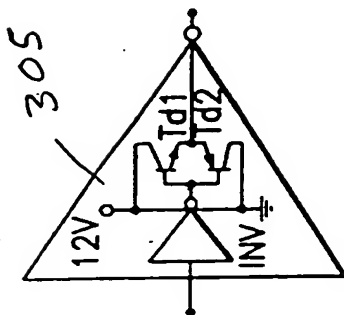


FIG. 3

PRIOR ART

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STATE

1

2

3

4

SOURCE  
12

NODE V1

NODE V2

NODE  
A

$V_p$

$I_L$

VG3

VG4

5V

0V

$V_{SS}$

0V

$V_{CC}$

$V_{SS}$

$V_{CC}$

$V_{SS}$

0V

$V_{CC}$

$V_{SS}$

0V

(+)

0A

(-)

$V_{CC}$

$V_{CC}-12$

12V

0V

t1 t2

t3

FIG 4

PRIOR ART

FIG 4 = PRIOR ART

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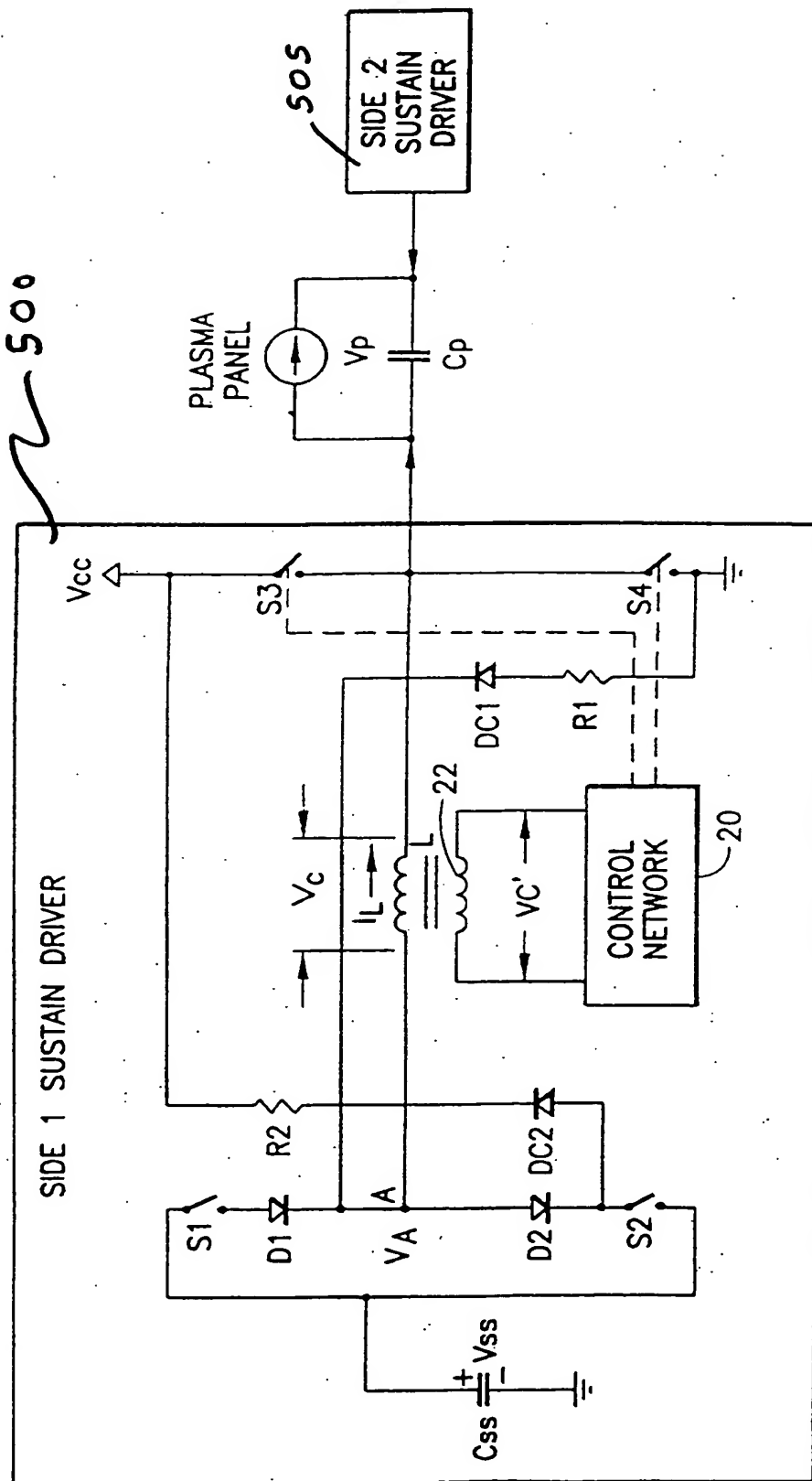


FIG. 5

PRIOR ART

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STATE

1

2

3

4

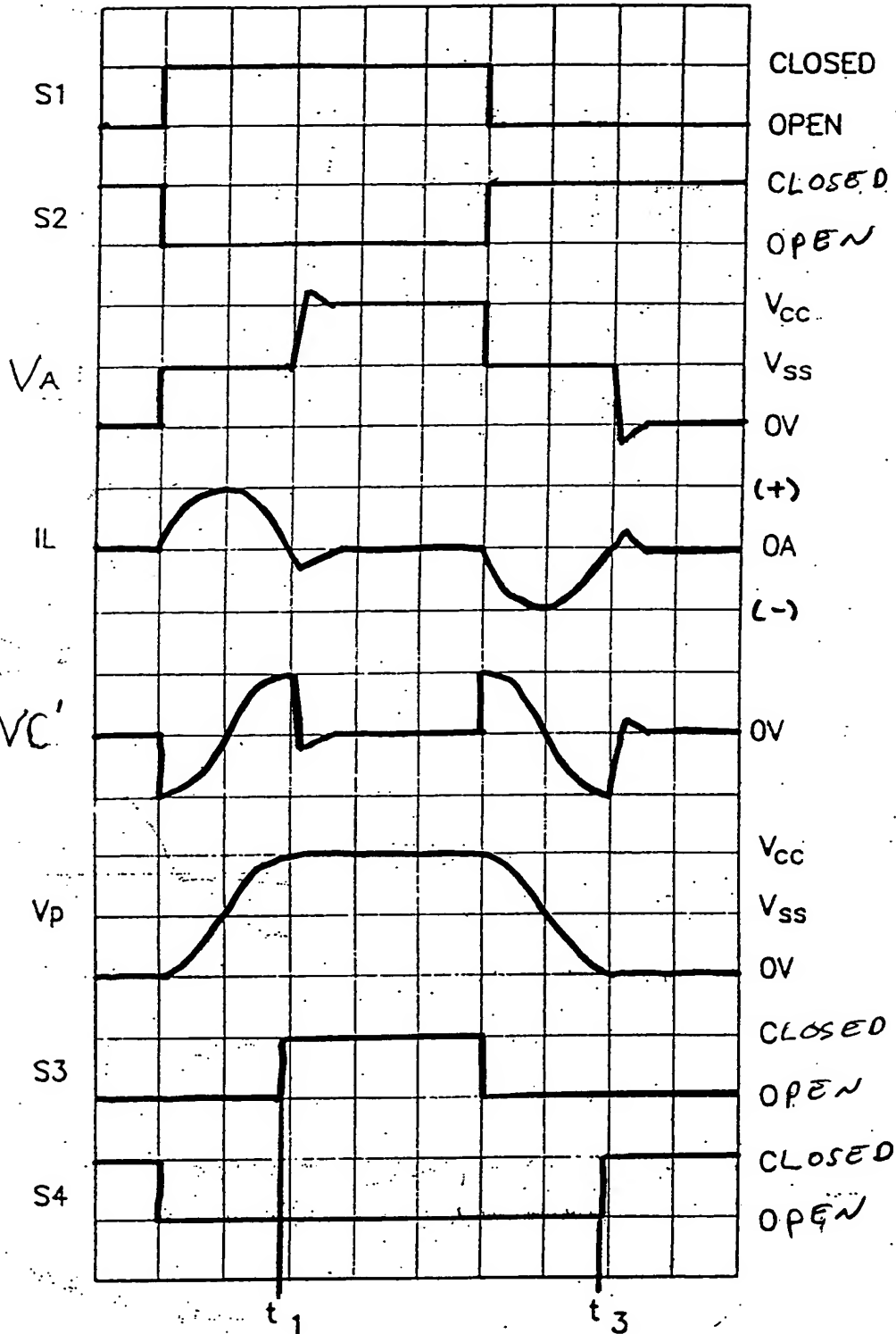


FIG. 16

Prior Art

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FIG. 7

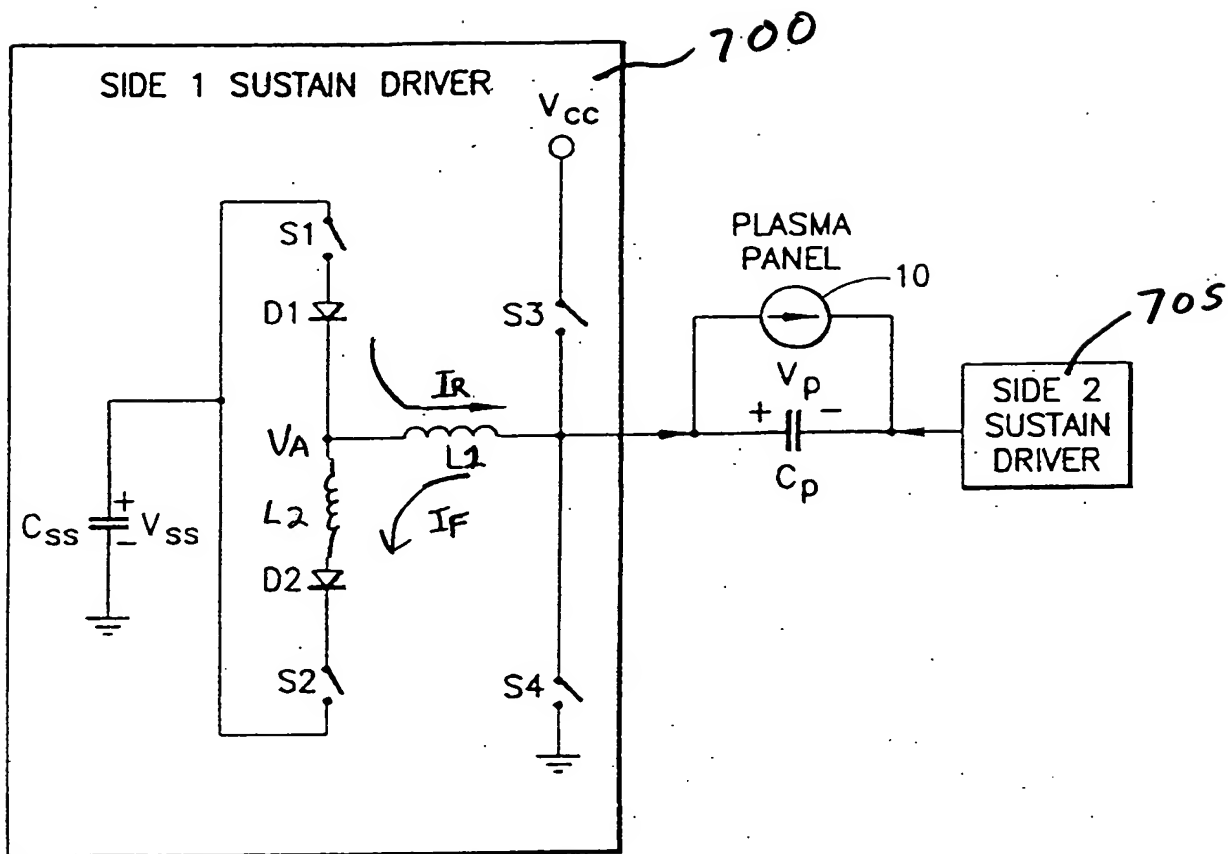
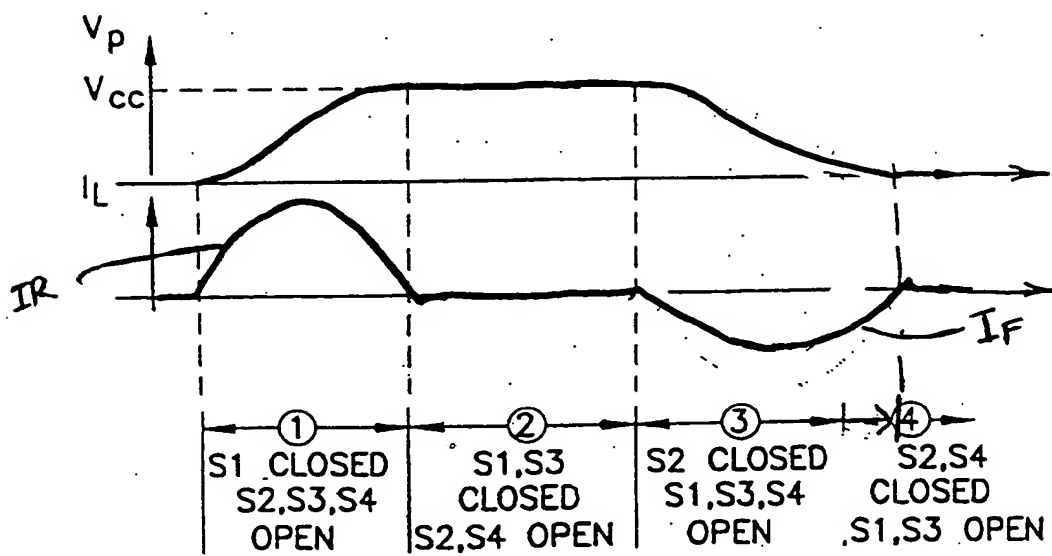


FIG. 8



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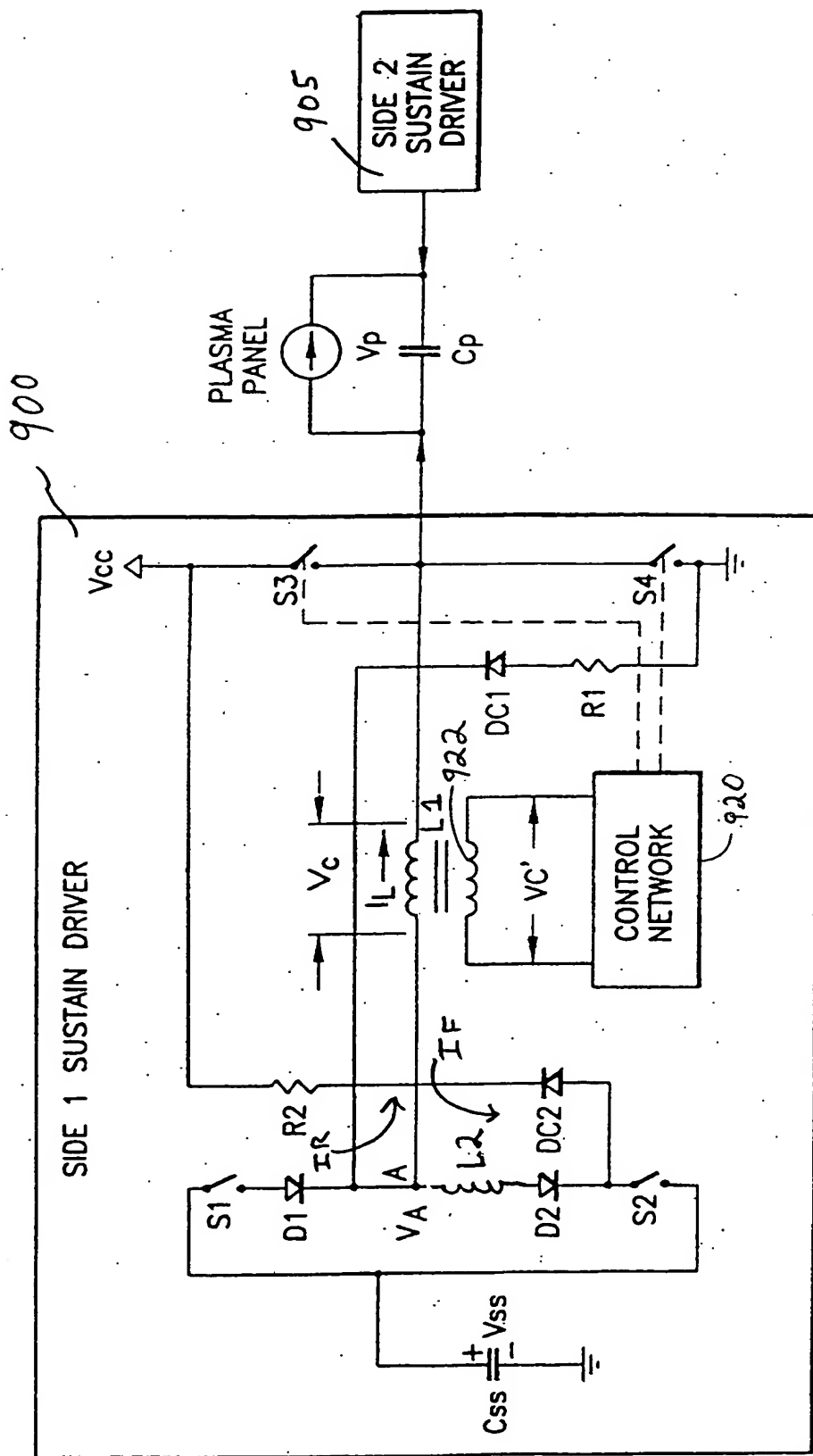


FIG. 9

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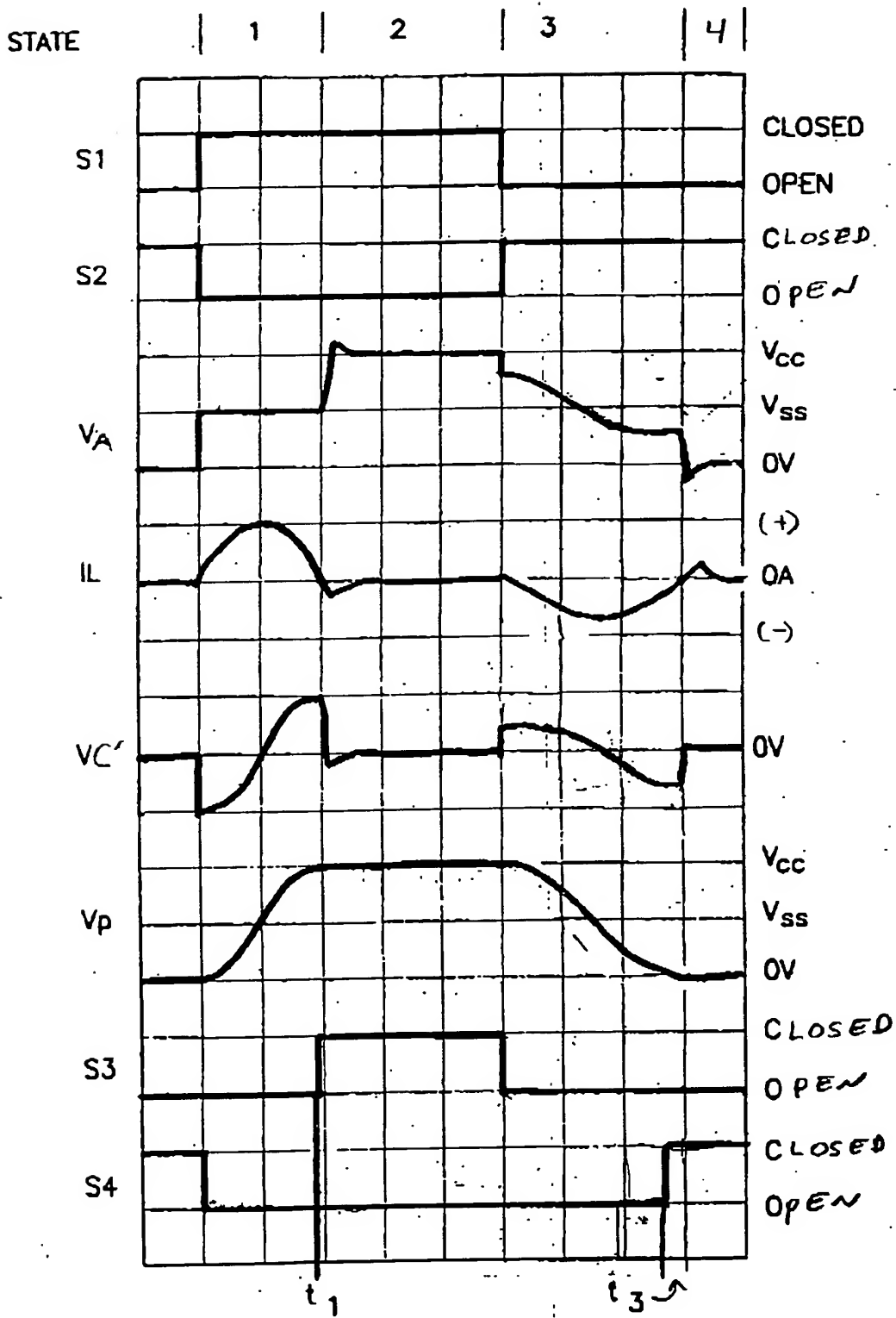


FIG. 10



[illegible]

FIG. 11

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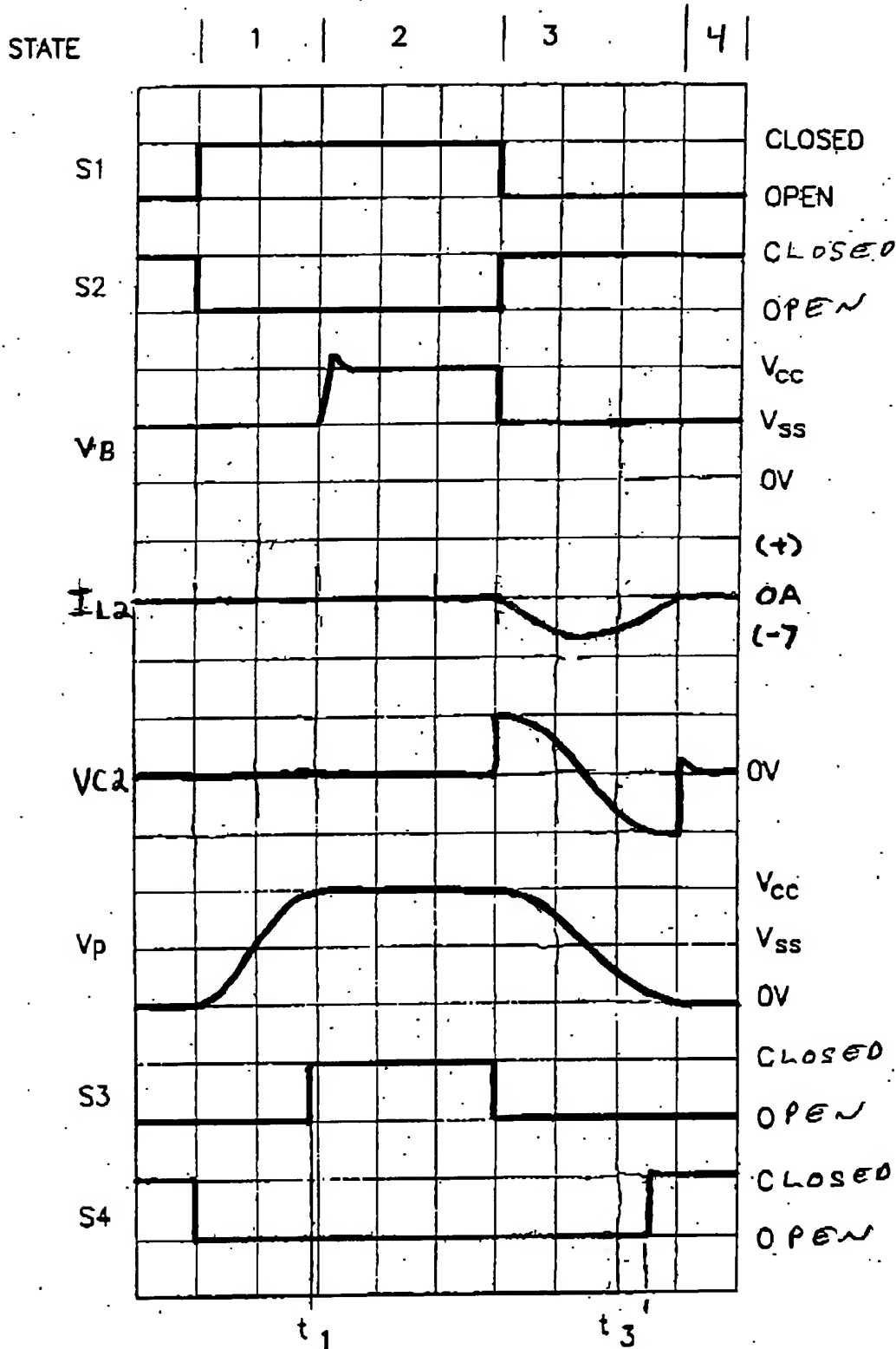


FIG. 12

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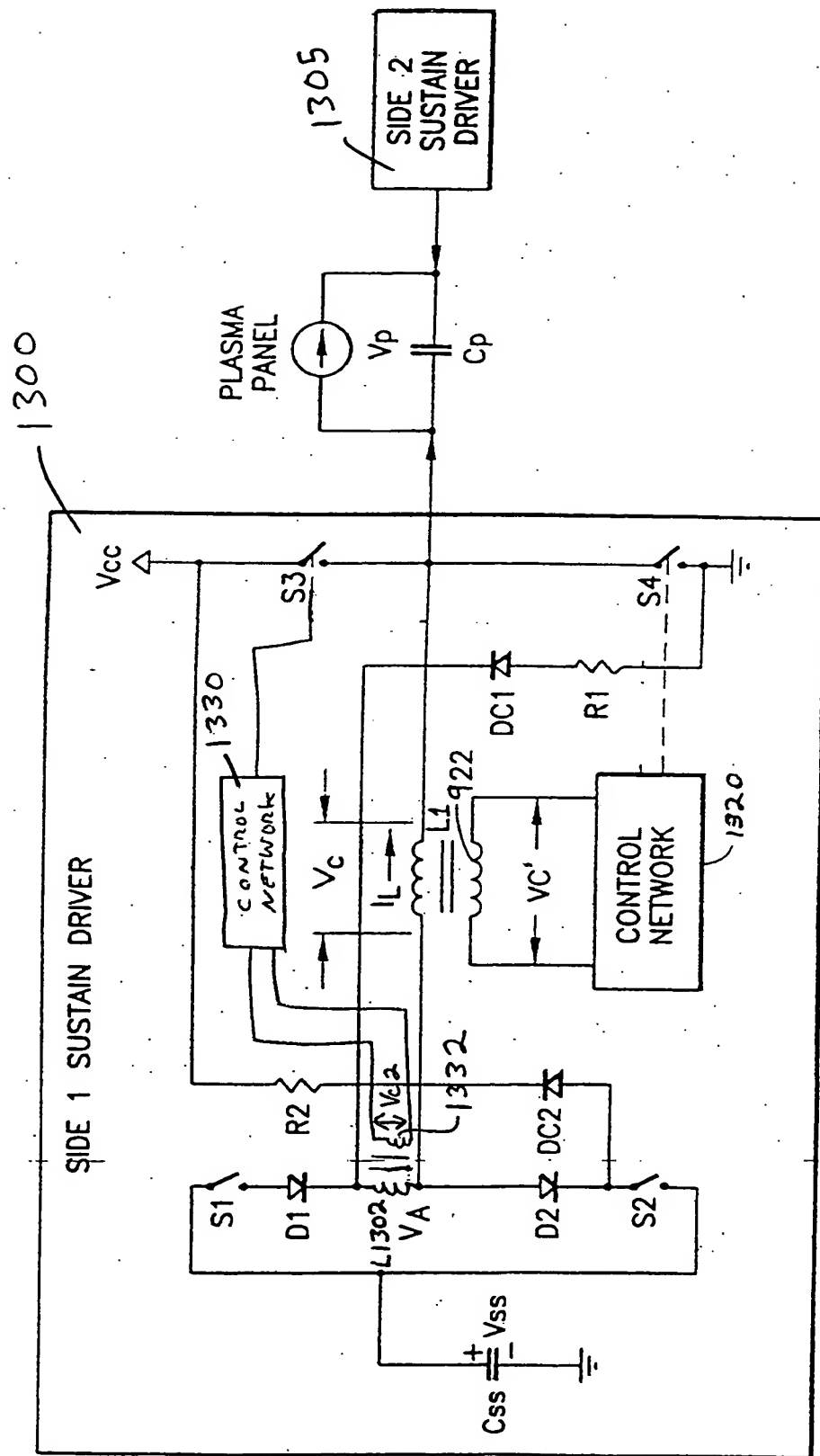


FIG. 13

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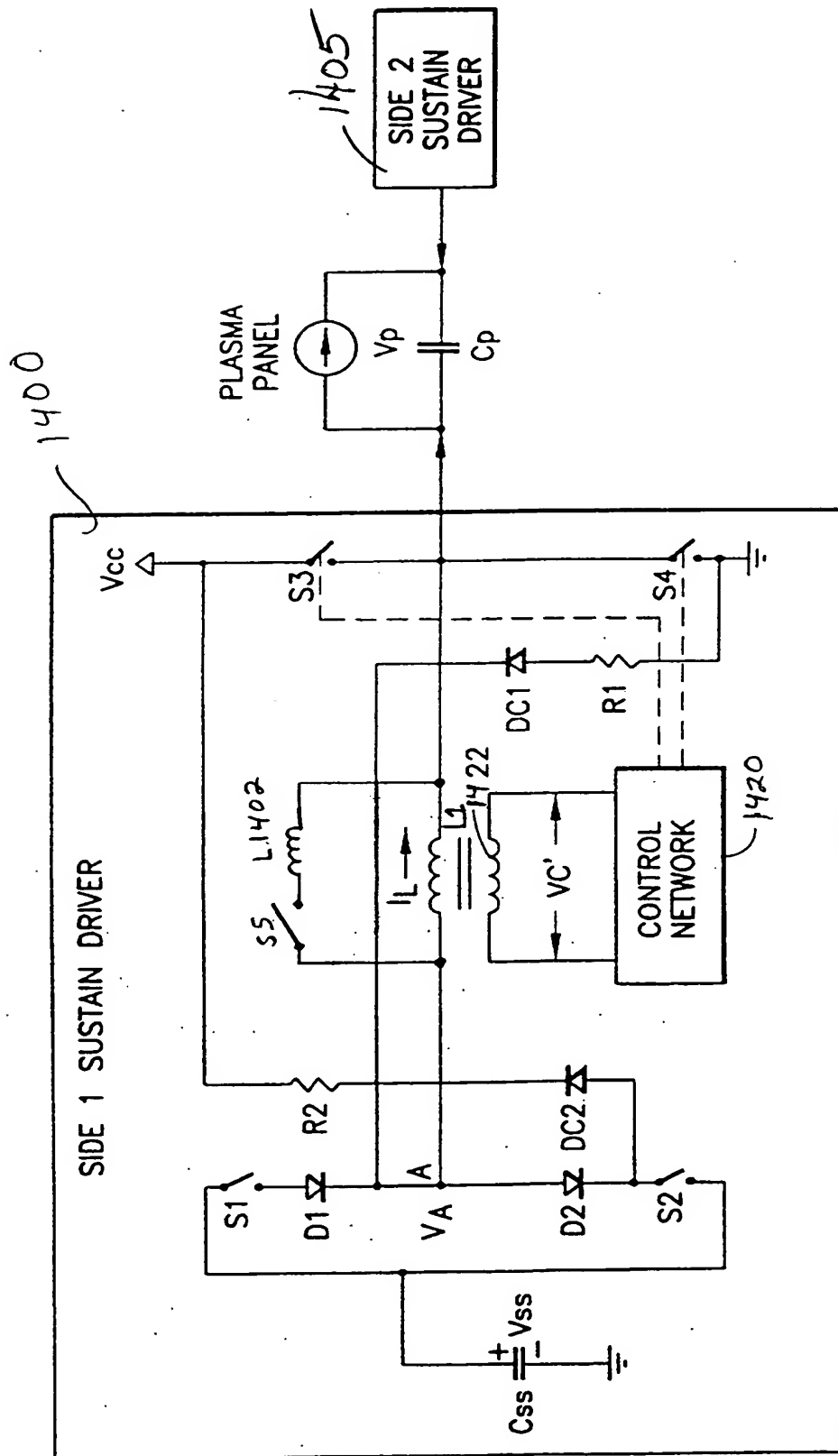


FIG. 14